

The New Costs of Physical Memory Fragmentation

DIMES '24

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 - Huge/giant pages
 - Redistribute between VMs/via CXL
 - or power down unused memory

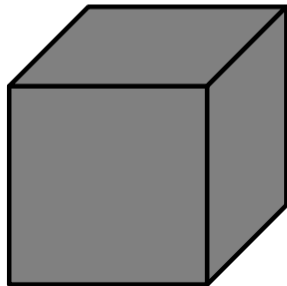
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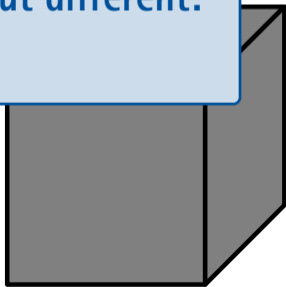
2MiB / 1GiB
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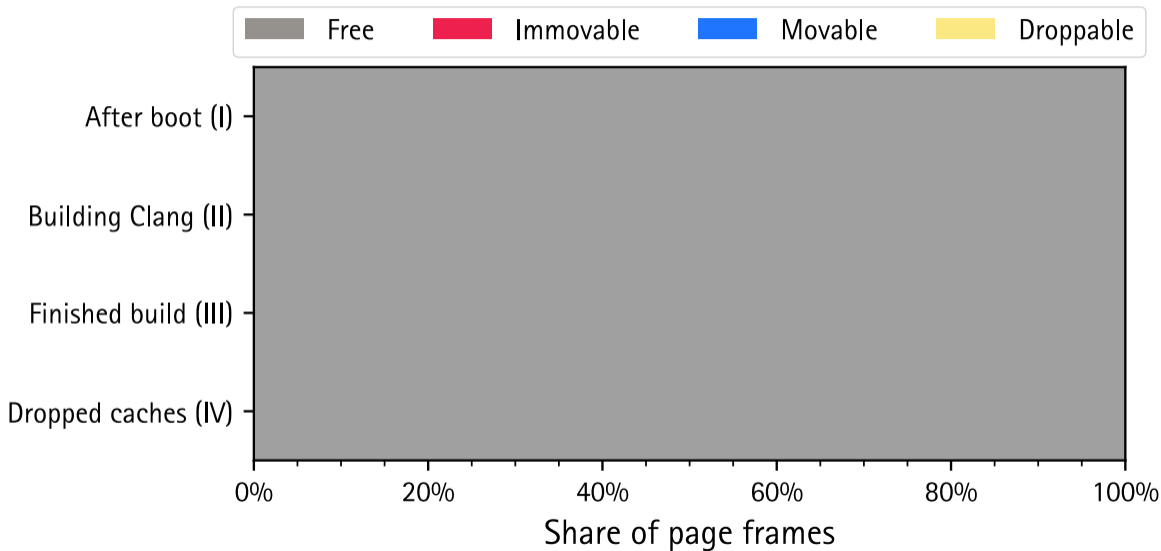
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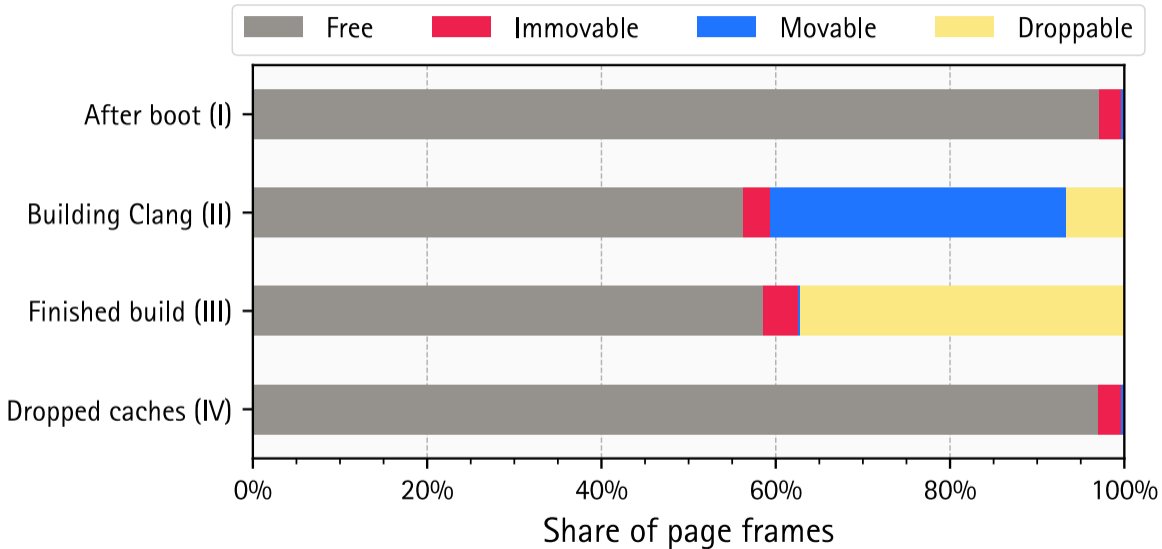
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 What we have

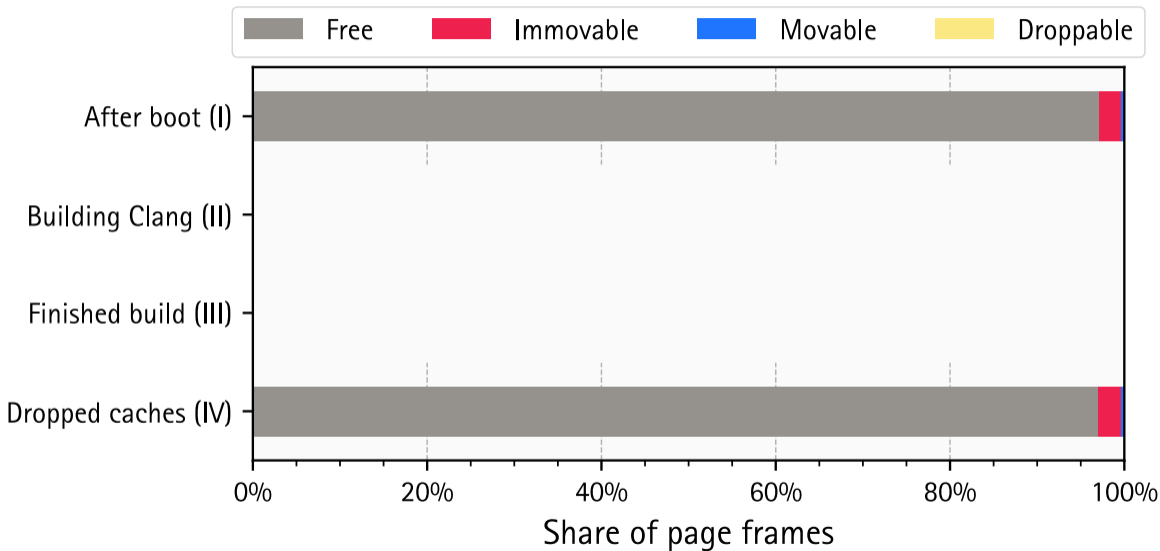


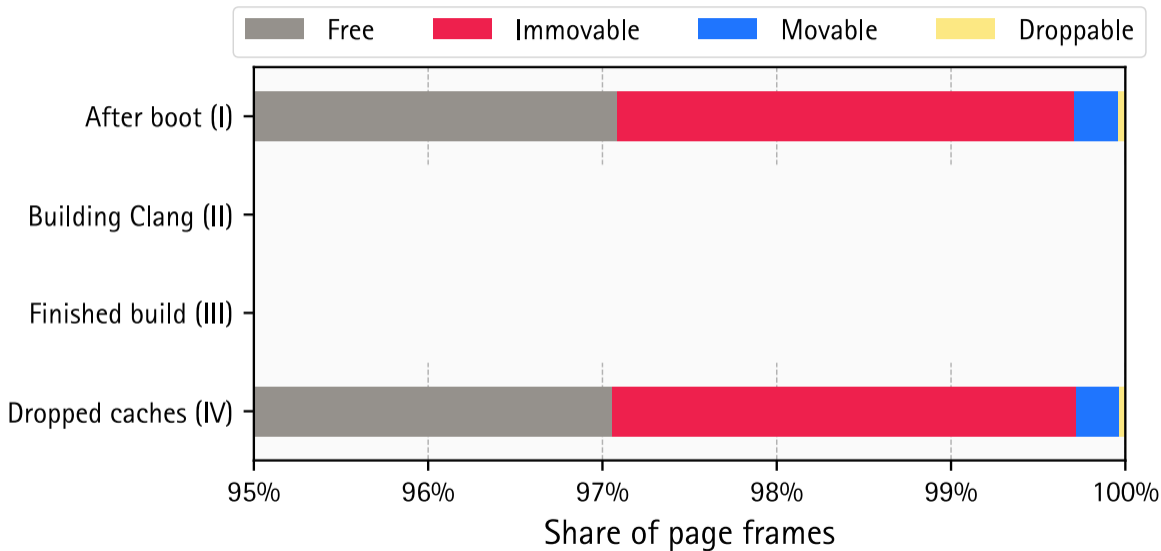
⇒ **The new fragmentation problem is real but different:
 We must think bigger!**

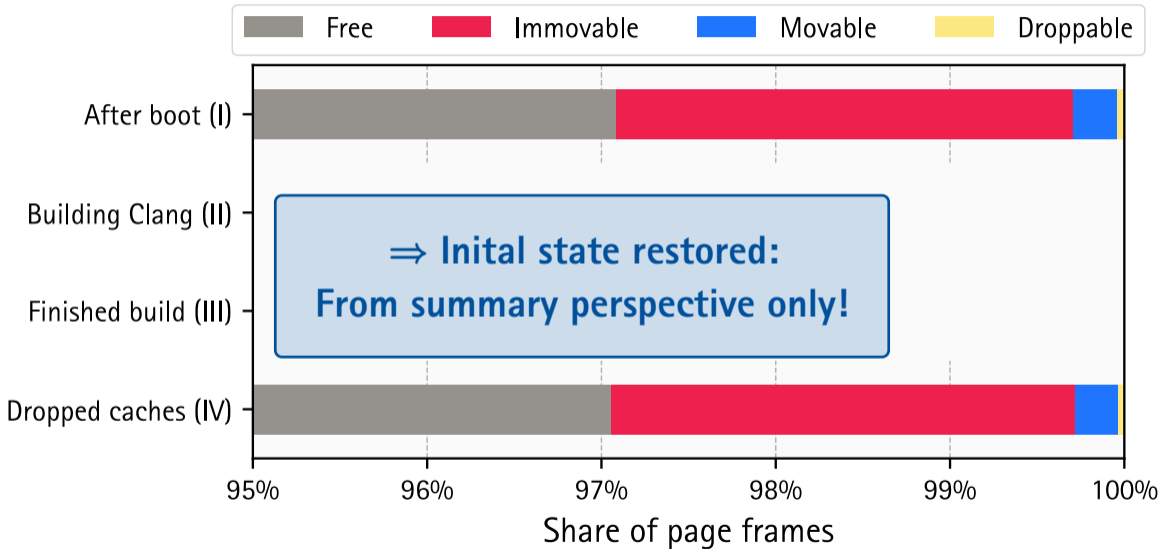
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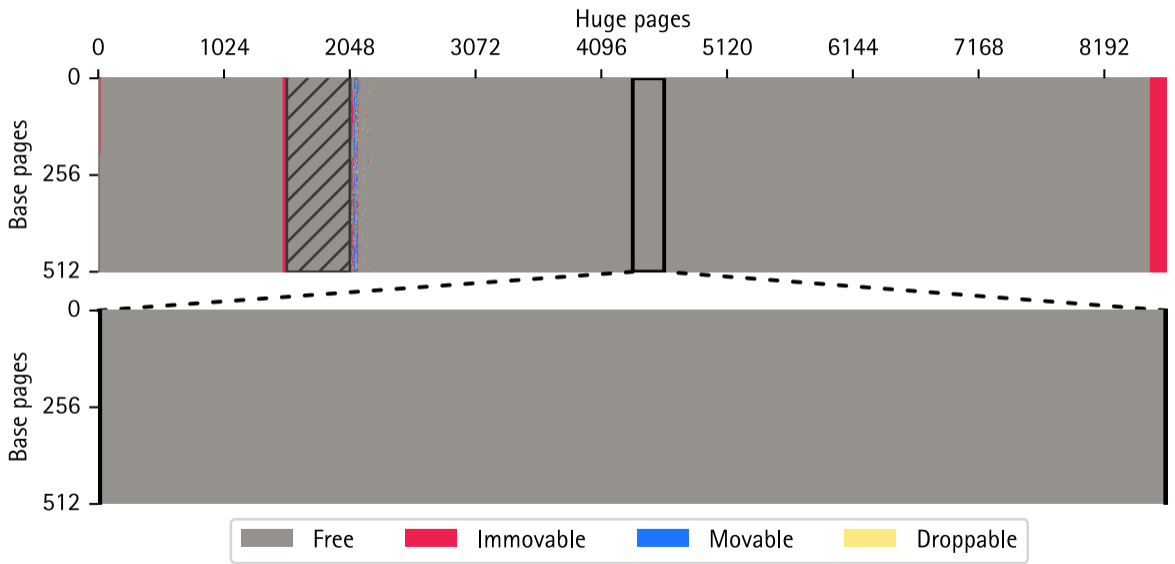


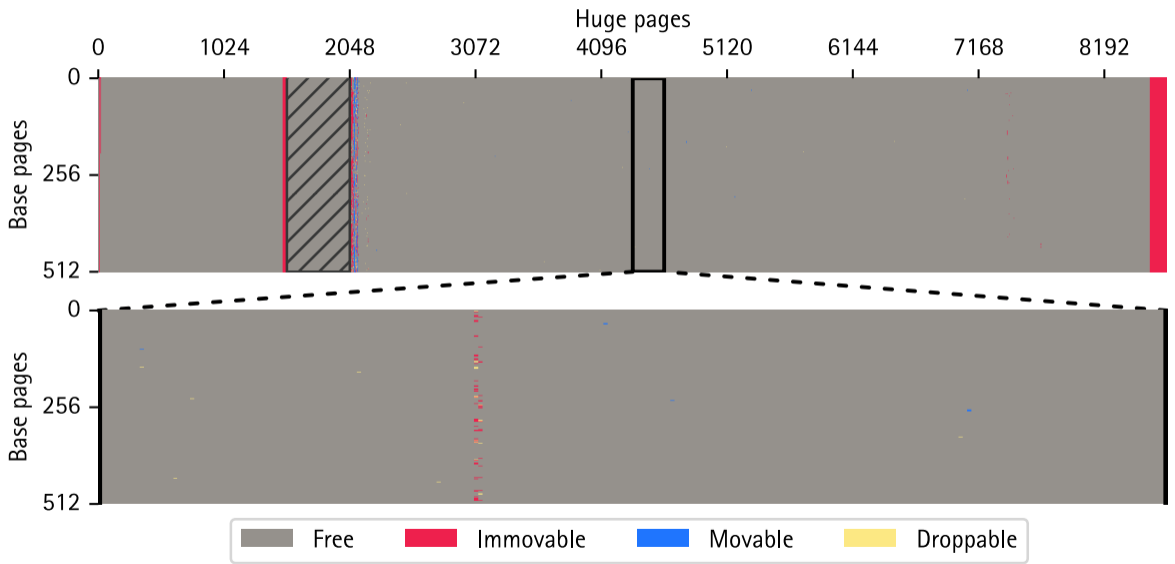


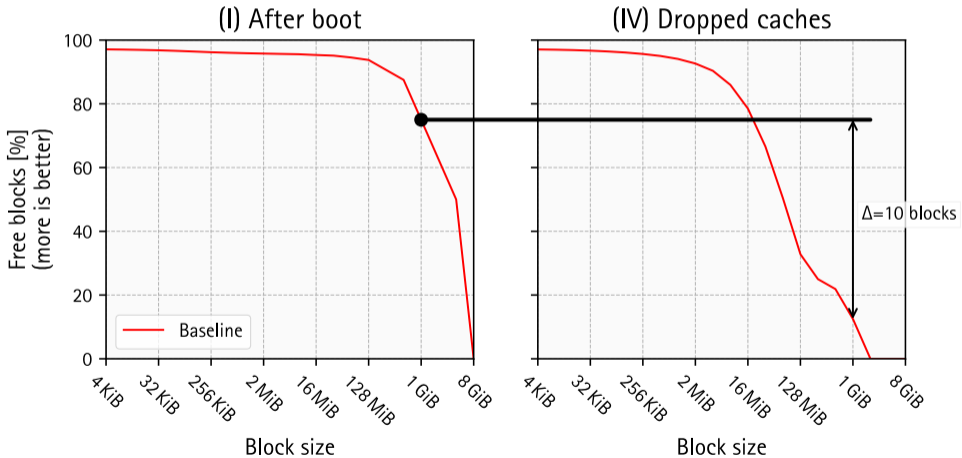


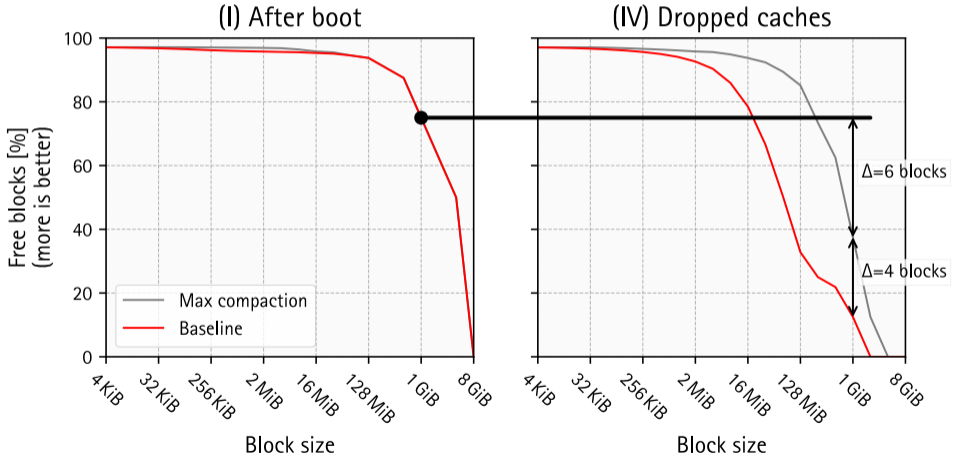






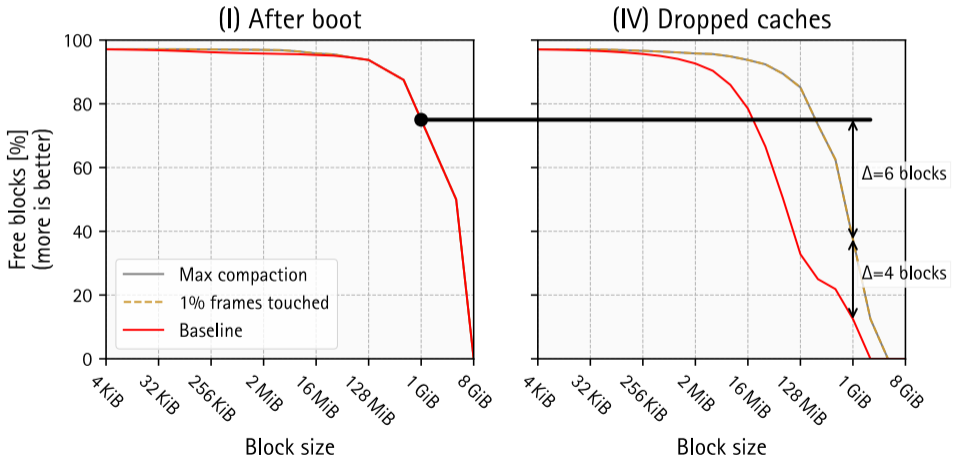




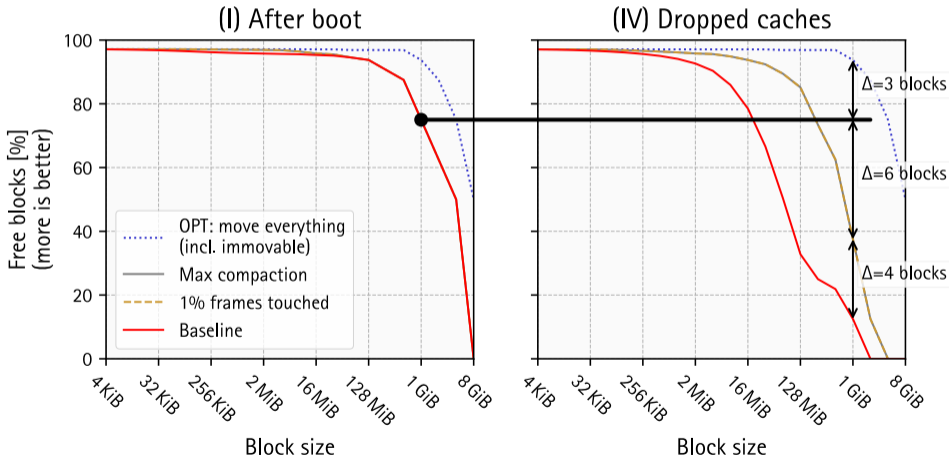


Case Study: Looking for a Solution

Cost function: $\#TOUCHes = \#DROPs + 2 \times \#MOVEs$



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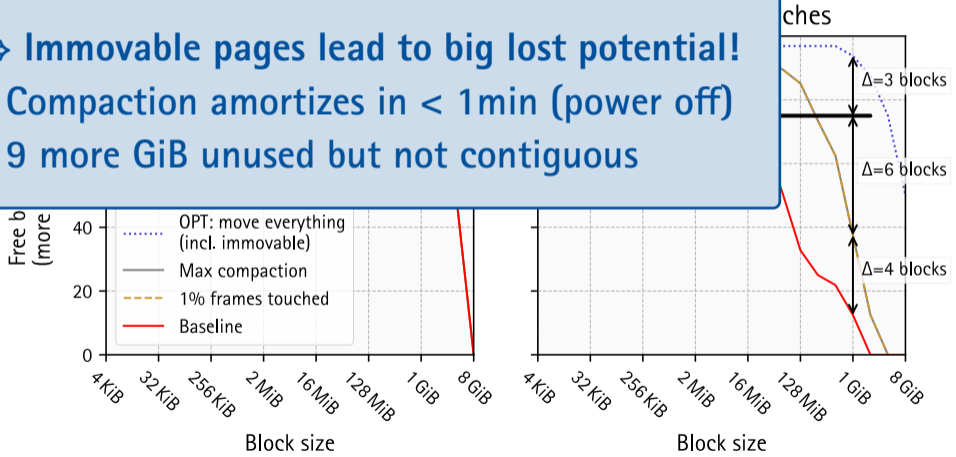


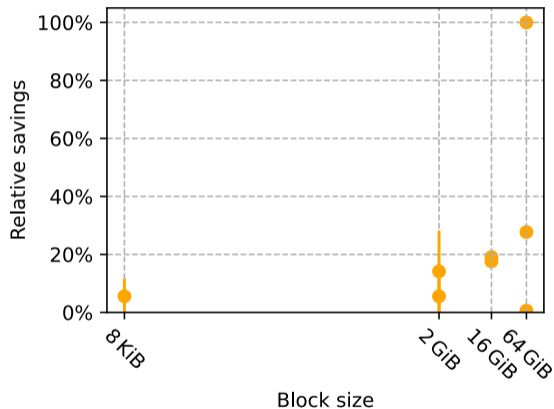
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⇒ **Immovable pages lead to big lost potential!**

- **Compaction amortizes in < 1min (power off)**
- **9 more GiB unused but not contiguous**





Example system

512 GiB DDR5

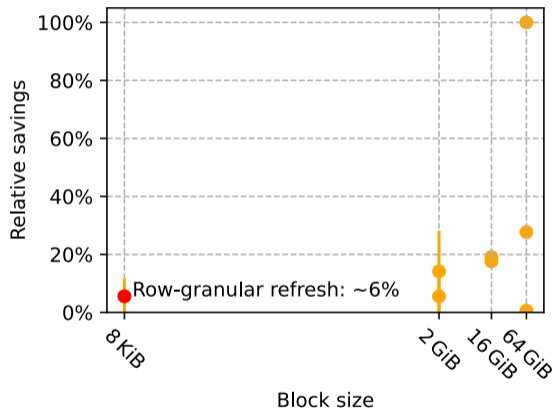
8 channels

4 ranks per channels

8×2 GiB chips per rank

32 banks per rank

65 536 rows per bank



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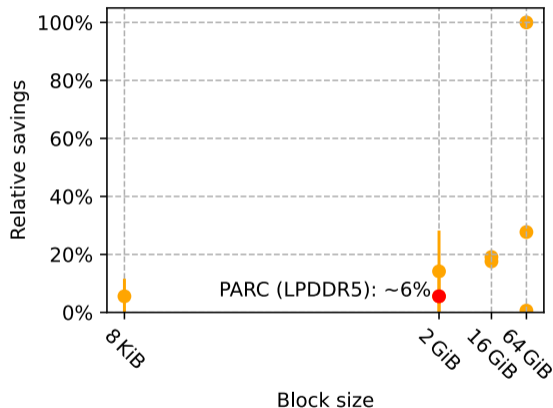
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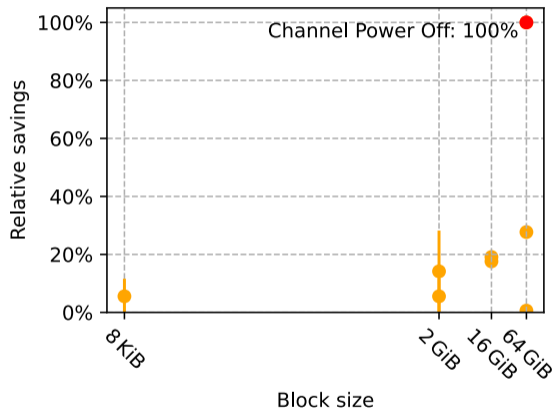
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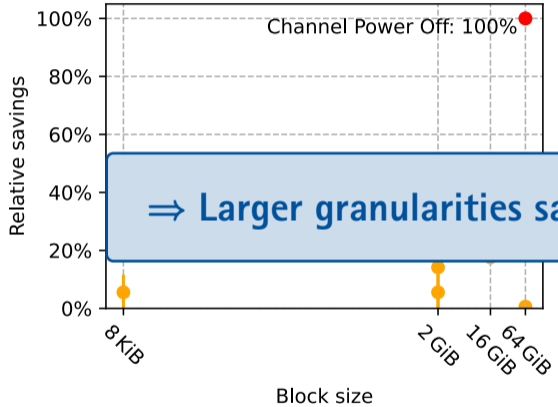
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DRAM Power Saving



Example system

- 512 GiB DDR5
- 8 channels
- 4 ranks per channels
- 16 banks per rank
- 65 536 rows per bank

- Linux is bad at managing physical memory
 - No fragmentation avoidance above (2 MiB) huge page size
 - Page cache unconditionally fills all available memory
 - Immovable pages make compaction impossible
- Using memory comes at a cost
 - Huge/giant page availability
 - Redistribute or turn off unused memory
 - Dynamic cloud pricing models



<https://sra.uni-hannover.de/p/dram-dimes24>